# Characterization of catastrophic faults in two-dimensional reconfigurable systolic arrays with unidirectional links 

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#### Abstract

The catastrophic fault pattern is a pattern of faults occurring at strategic locations that may render a system unusable regardless of its component redundancy and of its reconfiguration capabilities. In this paper, we extend the characterization of catastrophic fault patterns known for linear arrays to two-dimensional VLSI arrays in which all links are unidirectional. We determine the minimum number of faults required for a fault pattern to be catastrophic and give algorithm for the construction of catastrophic fault patterns with minimum number of faults.


Keywords: Catastrophic fault patterns; Fault tolerance; Algorithms

## 1. Introduction

Systolic systems have been widely used as parallel models of computation [5,7]. These systems consist of a large number of identical and elementary processing elements locally connected in a regular fashion. Each element receives data from its neighbors, computes and sends the results again to its neighbors. Few par-

[^0]ticular elements located at the extremes of the systems (these extremes depend on the particular system) are allowed to communicate with the external world. In this paper, we will focus on systolic two-dimensional array with unidirectional links.

Fault tolerant techniques are very important to systolic systems. Here we assume that only processors can fail. Indeed, since the number of processing elements is very large, the probability that a set of processing elements becomes faulty is fairly high. Thus, fault-tolerant mechanisms must be provided in order to avoid that faulty processing elements take
part in the computation. A widely used technique to achieve reconfigurability consists of providing redundancy to the desired architecture $[1,2]$. In systolic arrays the redundancy consists of additional processing elements, called spares, and additional connections, called bypass links. The redundant processing elements are used to replace any faulty processing element; the redundant links are used to bypass the faulty processing elements and reach others. The effectiveness of using redundancy to increase fault tolerance clearly depends on both the amount of redundancy and the reconfiguration capability of the system. It does however depend also on the distribution of faults in the system. There are sets of faulty processing elements for which no reconfiguration strategy is possible. Such sets are called catastrophic fault patterns (CFPs).

If we have to reconfigure a system when a fault set occurs, it is necessary to know if the set is catastrophic or not. Therefore it is important to study the properties of catastrophic sets. Till today, the characterization of CFPs is known only for linear arrays with the following results. The characterization has been used to obtain efficient testing algorithms both for unidirectional and bidirectional cases [9] with order of magnitude improvement over [3,4]. Efficient techniques have been obtained for constructing CFPs [8]. Recently, using random walk as a tool, a closed form solution for the number of CFPs for bidirectional links has been provided in [6], an improvement over [11,4].

The main contribution of this paper is complete characterization of catastrophic fault patterns for twodimensional arrays with unidirectional links. The requirement on the minimum number of faults in a fault pattern for it to be catastrophic is shown to be a function of the length of the longest horizontal bypass link and the number of rows in the two-dimensional array. From a practical viewpoint, above result allows to prove some answers to the question about the guaranteed level of fault tolerance of a design. Guaranteed fault tolerance indicates positive answer to the question as: will the system withstand up to $k$ faults always regardless of how and where they occur? We analyze catastrophic faults having the minimal number of defective processors. Throughout this paper, by catastrophic fault pattern we mean a catastrophic fault pattern having minimum number of faulty processing elements. We describe algorithm for constructing a catastrophic fault pattern with maximum width
(i.e., the distance between the first and the last fault in the fault pattern). The width of a fault pattern must fall with in precise bounds for the pattern to be catastrophic. This algorithm gives us the framework for achieving the upper bounds on the width of a catastrophic fault patten for different link configurations.

## 2. Preliminaries

In this paper, we will focus on two-dimensional networks. The basic components of such a network are the processing elements (PEs) indicated by circles in Fig. 1. The links are unidirectional. There are two kinds of links: regular and bypass. Regular links connect neighboring (either horizontal or vertical) PEs while bypass links connect non-neighbors. The bypass links are used strictly for reconfiguration purposes when a fault is detected, otherwise they are considered to be the redundant links. We now introduce the following definitions:

Definition 2.1. A two-dimensional network $\mathcal{N}$ consists of a set $V$ of PEs and a set $E$ of links (where a link joins a pair of distinct PEs) satisfying the conditions listed below.
$V$ is the union of three disjoint sets: the set $I C U L=$ $\left\{I C U L_{1}, I C U L_{2}, \ldots, I C U L_{N_{1}}\right\}$ of left interface control units, the set $I C U R=\left\{I C U R_{1}, I C U R_{2}, \ldots, I C U R_{N_{1}}\right\}$ of right interface control units and a two-dimensional array $A=\left\{p_{i j}: 1 \leqslant i \leqslant N_{1}, 1 \leqslant j \leqslant N_{2}\right\}$ of PEs. We sometimes refer to the processing element $p_{i j}$ as $(i, j)$.
$E$ consists of the links obtained as follows. Fix integers $1=g_{1}<g_{2}<\cdots<g_{k} \leqslant N_{2}-1$ and $1=v_{1}<$ $v_{2}<\cdots<v_{l} \leqslant N_{1}-1$. Join $p_{i j}$ to $p_{i^{\prime} j^{\prime}}$ by a link if and only if
(i) $i=i^{\prime}$ and $\left(j-j^{\prime}\right)$ is one of $g_{1}, g_{2}, \ldots, g_{k}$ or
(ii) $j=j^{\prime}$ and $\left(i-i^{\prime}\right)$ is one of $v_{1}, v_{2}, \ldots, v_{l}$.

Also join $I C U L_{i}$ to $p_{i 2}, \ldots, p_{i g_{k}}$ and join $p_{i, N_{2}-g_{k}+1}$, $p_{i, N_{2}-g_{k}+2}, \ldots, p_{i, N_{2}-1}$ to $I C U R_{i}$ by links, for $i=$ $1,2, \ldots, N_{1}$.

We assume that $N_{1}>v_{l}$ and $N_{2}>g_{k}\left(N_{1}-1\right)+$ $\left(g_{k}-1\right)^{2}+1$. The idea behind this specific choice of lower bound for $N_{2}$ is given in Result 3.1.


Fig. 1. Two-dimensional network of PEs.

Definition 2.2. We call $g_{1}, g_{2}, \ldots, g_{k}$ the horizontal link redundancies of $\mathcal{N}$ and $v_{1}, v_{2}, \ldots, v_{l}$ the vertical link redundancies of $\mathcal{N}$. We refer to $G=$ $\left(g_{1}, g_{2}, \ldots, g_{k} \mid v_{1}, v_{2}, \ldots, v_{l}\right)$ as the link redundancy of $\mathcal{N}$.

Fig. 1 shows a two-dimensional network with $N_{1}=4, N_{2}=15$ and $G=(1,3 \mid 1,2)$. A link joining two PEs of the type $p_{i j}$ and $p_{i, j+1}$ is called a horizontal direct link and a link joining two PEs of the type $p_{i j}$ and $p_{i+1, j}$ is called a vertical direct link. Direct links are also called regular links. Links joining $p_{i j}$ and $p_{i, j+g}$ with $g>1$ are called horizontal bypass links and links joining $p_{i j}$ and $p_{i+v, j}$ with $v>1$ are called vertical bypass links.

The length of the horizontal bypass link joining $p_{i j}$ to $p_{i, j+g}$ is $g$ and the length of the vertical bypass link joining $p_{i j}$ to $p_{i+v, j}$ is $v$.

We assume that the direction of information flow through horizontal links is from left to right and the direction of information flow through vertical links is from top to bottom of the array. Note that, no links exist in the network $\mathcal{N}$ except the ones specified by $G$ as in Definition 2.1. It is assumed that ICUL and ICUR always operate correctly and we are considering information flow from ICUL to ICUR.

Definition 2.3. Given a two-dimensional array $A$, a fault pattern (FP) for $A$ is simply a non-empty subset $F$ of the set of processing elements in $A$. An assignment of a fault pattern $F$ to $A$ means that every processing element belonging to $F$ is faulty (and the others operate correctly).

Given a fault pattern $F$, define $m=\min \{j$ : $(i, j) \in F\}$ and $M=\max \{j:(i, j) \in F\}$.

Definition 2.4. The window $W_{F}$ of a fault pattern $F$ is the sub-array of $A$ consisting of $\left\{p_{i j}: 1 \leqslant i \leqslant N_{1}\right.$, $m \leqslant j \leqslant M\}$. By the width $\left\|W_{F}\right\|$ of $F$ we mean $M-m+1$.

Definition 2.5. A fault pattern is catastrophic for the network $\mathcal{N}$ if ICUL and ICUR are not connected (i.e., there is no path connecting any $I C U L_{i}$ to any $\operatorname{ICUR} i^{\prime}$ which does not involve a faulty PE ) when the fault pattern $F$ is assigned to $A$.

Example 2.1. Consider the fault pattern $F=\{(1,5)$, $(1,6),(1,8),(1,11),(2,5),(2,8),(2,10),(2,11)$, $(3,6),(3,8),(3,9),(3,11),(4,7),(4,8),(4,10)$, $(4,13)\}$ with unidirectional link redundancy $G=$ $(1,4 \mid 1)$ in a $4 \times 24$ array as shown in Fig. 2. Links and some processors are not drawn in the figure.

We see from Fig. 3, that the removal of the processing elements belonging to $F$ along with their inci-


Fig. 2. Fault pattern $F$.


Fig. 3. Network $\mathcal{N}$ after the removal of $F$ and their incident links.
dent links disconnects $I C U L$ and $I C U R$. Hence $F$ is catastrophic. It is easy to check that $F$ remains catastrophic with respect to unidirectional link redundancy $G=(1,4 \mid 1,2)$.

## 3. Characterization of catastrophic fault patterns

In this section, we will characterize the catastrophic fault patterns for two-dimensional networks and prove that the minimum number of faults in a catastrophic fault pattern is a function of $N_{1}$ and the length of the longest horizontal bypass link.

Theorem 3.1. $F$ is catastrophic with respect to $\mathcal{N}$ implies that the cardinality of $F,|F| \geqslant N_{1} g_{k}$.

Proof. Suppose to the contrary that $|F|<N_{1} g_{k}$. Then partition the two-dimensional array $A$ of PEs into blocks of $g_{k}$ columns as $A=\left(A_{1} \vdots A_{2} \vdots \ldots \vdots A_{c}\right)$ where $c=\left\lceil N_{2} / g_{k}\right\rceil$ and place the blocks as consecutive floors to form a cuboid as shown in Fig. 4. Observe that, in this cuboid representation, each horizontal regular link joins two consecutive elements in the same row of a floor or the last element of a row of a floor with the first element of the same row of the floor just above it whereas each vertical regular link joins two consecutive elements in the same column of a floor. On the other hand, each horizontal bypass link of the maximum length joins two consecutive elements in the same pillar. So, in this cuboid, going up along a pillar corresponds to using the longest horizontal bypass links. Since the number of faulty elements $|F|$ is less than the number of pillars, there must be a pillar with no faulty element, regardless of the distribution of the fault pattern. Since the bottom and top of each pillar are linked to ICUL and ICUR, respectively, $F$ cannot be catastrophic since we can use the bypass links of length $g_{k}$ to avoid the faulty PEs, a contradiction which proves the theorem.

This theorem gives us a necessary condition on the minimum number of faults required for blocking a two-dimensional array. This also tells us that fewer than $N_{1} g_{k}$ faults occurring in $A$ will not be catastrophic. In the following we will restrict ourselves to the case where there are at least $N_{1} g_{k}$ faults, and we


Fig. 4. Cuboid representation of a $4 \times 15$ array with link redundancy $G=(1,3 \mid 1)$.
will characterize the blocking fault patterns containing exactly $N_{1} g_{k}$ faults.

Not all fault patterns consisting of $N_{1} g_{k}$ faults are catastrophic. Some additional properties must be satisfied. Before we describe further characteristics of a catastrophic fault pattern, we outline an algorithm for the construction of a CFP with the maximum width for a given link redundancy $G$ when links are unidirectional. Recall that, $N_{2}$ is very large compare to $g_{k}\left(N_{1}-1\right)+\left(g_{k}-1\right)^{2}+1$ and $N_{1}>v_{l}$. For simplicity, we also assume that $N_{2}$ is divisible by $g_{k}$.

Algorithm UCFP. Construction of a catastrophic fault pattern for unidirectional horizontal and unidirectional vertical links.

## Input: G.

Output: A catastrophic fault pattern $F$ with the maximum width.

Step 1. Partition the two-dimensional array of PEs into blocks of $g_{k}$ columns and list the blocks as the floors of a cuboid. Mark the first element of the $N_{1}$ th row in floor 0 by an $X$ and set $f=1$.

Step 2. If there exists an unmarked element $u=$ $(i, j)$ in floor $f$ such that the element $v=\left(i, j-g_{k}\right)$ below $u$ in floor $f-1$ is marked, choose one such $u$ and go to Step 4. Otherwise go to Step 3.

Step 3. If there is an unmarked element in floor $f$, then increase $f$ by 1 and go to Step 2. Otherwise, go to Step 5.

Step 4. If $v$ is marked $Y$, then mark $u$ by $Y$ and go to Step 2. If $v$ is marked $X$, then mark $u$ by $Y$ and mark every unmarked element $w$ which is of the form $(i, j-g)$ where $g \in\left\{g_{1}, g_{2}, \ldots, g_{k-1}\right\}$ or $(i-v, j)$ where $v \in\left\{v_{1}, v_{2}, \ldots, v_{l}\right\}$. Mark $w$ by $Y$ if the pillar of $w$ contains another marked element; otherwise mark $w$ by $X$. Go to Step 2 .

Step 5. Stop. Note that all elements in floor $f$ are marked. The elements marked $X$ form a catastrophic fault pattern $F$ with maximum width for link redundancy $G$.

Observations. (1) Note that the algorithm assigns exactly $N_{1} g_{k}$ number of $X$ 's.
(2) In Step 3, if there is an unmarked element in floor $f$, then there always exists a floor above floor $f$. The reason is this. Since $N_{2}>g_{k}\left(N_{1}-1\right)+\left(g_{k}-\right.$ $1)^{2}+1$, there are at least $N_{1}+g_{k}-3$ floors in the cuboid representation. Note that, the elements in floor $N_{1}+g_{k}-3$ are all marked. Therefore, a floor $f$ with some unmarked elements must be below the floor $N_{1}+g_{k}-3$, and hence there exists a floor above that floor.

Example 3.1. Fig. 5 shows a CFP obtained by the above algorithm consisting of 16 faults corresponding to $G=(1,4 \mid 1,3)$ in a $4 \times 24$ array $A$ when the links are unidirectional.

Theorem 3.2. Algorithm UCFP generates a catastrophic fault pattern.

Proof. We make the following simple observations on the algorithm:






Fig. 5. Fault pattern for $G=(1,4 \mid 1,3)$.

- Marking takes place only in Step 4. When some PE is marked there are two cases: (a) if the pillar has no marked PE then the current PE is marked by $X$ and (b) if the pillar has at least one marked PE then all marked PEs are below the current PE and the current PE is marked by $Y$. When the algorithm terminates, we also have the following:
- There is exactly one $X$ in each pillar, hence $|F|=$ $N_{1} g_{k}$.
- For each pillar the $X$ occurs below the $Y$ 's.
- Let the final value of $f$ be $f_{0}$. If a PE $p$ is marked $Y$ and is adjacent to a PE $q$ then $q$ is marked (with $X$ or $Y$ ) unless $p$ is in floor $f_{0}$ and $q$ is in a floor $f_{0}+1$.

We next prove that any PE $\omega_{1}$ marked with a $Y$ is inaccessible, i.e., there is no way to reach this PE from ICUL without using any faulty PEs (those marked
with $X$ ). Suppose $\omega_{1}$ is accessible, i.e., $\omega_{1}$ is connected to ICUL by a path $\mu=\left[I C U L, \ldots, \omega_{2}, \omega_{1}\right]$ not containing any faulty PEs (those marked $X$ ). We consider two cases:

Case 1. $\omega_{1}, \omega_{2}, \ldots$ are all marked $Y$. Now, the PEs adjacent to ICUL all lie in the Oth floor. But no PE in the 0th floor can be marked $Y$ by the algorithm, a contradiction which proves that $\omega_{1}$ is not accessible from ICUL.

Case 2. $\omega_{1}, \omega_{2}, \ldots, \omega_{i}$ are marked $Y$ and $\omega_{i+1}$, $\omega_{i+2}, \ldots$ are unmarked. But there is no direct link from an unmarked PE to a PE marked with a $Y$, a contradiction which proves that $\omega_{1}$ is not accessible from $I C U L$.

Clearly when the algorithm terminates, all PEs in floor $f_{0}$ are either faulty or inaccessible from $I C U L$, so no PE in any floor $\geqslant f_{0}+1$ is accessible from $I C U L$; in particular, ICUR is not accessible from ICUL and $F$ is a CFP.

We prove that the algorithm terminates by showing that the number of marked elements in a floor increases strictly until it reaches $N_{1} g_{k}$. Note that all elements of the floor $f+1$, which are above marked elements in floor $f$, are marked. If there is an unmarked element in the $f$ th floor, note that some marked element $v$ is adjacent to an unmarked element $z$ in the $f$ th floor. Then, by the algorithm, the element $w$ in floor $f+1$ above $z$ will be marked.

Conjecture 3.1. The catastrophic fault pattern $F$, generated by Algorithm UCFP, has the maximum width of the window $W_{F}$.

We now indicate our basis for the above conjecture. Let $F^{*}$ be any catastrophic fault pattern with $N_{1} g_{k}$ faulty PEs. We assume that PE ( $N_{1}, 1$ ) belongs to $F^{*}$. We now consider the cuboid representation of $A$ used in the proof of Theorem 3.1. Note that, since there are only $N_{1} g_{k}$ faulty PEs, each pillar can contain only one faulty PE. Now mark the faulty PEs by $X$ 's and in each pillar mark the PEs above the faulty PE by $Y$ 's. A PE $(i, j)$ marked by $X$ and $\neq\left(N_{1}, 1\right)$ is called fair PE if no PE among the PEs $(i, j-g)$ where $g \in\left\{g_{1}, g_{2}, \ldots, g_{k-1}\right\}$ or $(i-v, j)$ where $v \in$ $\left\{v_{1}, v_{2}, \ldots, v_{l}\right\}$ is marked by $Y$. In other words, a PE (marked by $X$ ) is called fair PE if it is not accessible from any PE marked by $Y$ using the links in $G$. In the given fault pattern $F^{*}$, if there exists a fair $\operatorname{PE}(i, j)$, we obtain a new fault pattern called derived fault pat-
tern by making the $\mathrm{PE}\left(i, j+g_{k}\right)$ faulty instead of PE $(i, j)$. Then PE $\left(i, j+g_{k}\right)$ becomes faulty whereas PE ( $i, j$ ) becomes accessible from ICUL. Note that, the derived fault pattern is still catastrophic and its width is greater than or equal to the width of $F^{*}$. If there is any fair PE $\left(i^{\prime}, j^{\prime}\right)$ in the derived fault pattern then make PE ( $i^{\prime}, j^{\prime}+g_{k}$ ) faulty instead of PE ( $i^{\prime}, j^{\prime}$ ). Continuing this process we get a derived fault pattern $F^{0}$ with no fair PE. It is easy to check that this process terminates in finite number of steps. Clearly $\left\|W_{F^{0}}\right\| \geqslant\left\|W_{F^{*}}\right\|$. Let $F$ be the fault pattern obtained from Algorithm UCFP. Then we think $F$ will be the only catastrophic fault pattern which does not have any fair PE, so $F^{0}=F$ and $\left\|W_{F}\right\| \geqslant\left\|W_{F^{*}}\right\|$. Since $F^{*}$ is arbitrary, $F$ has the maximum width.

This conjecture gives us the framework for achieving specific upper bounds and exact bounds on the size of the largest window for a given link configuration. Given a link configuration $G$, we can obtain, by applying the algorithm, a catastrophic fault pattern $F$ which is contained in the largest window; that is, $W_{F}$ is the maximum value possible.

We study the effect of $G$ on the maximum width of window of a CFP. We start by showing that the window size decreases as the size of $G$ increases.

Theorem 3.3. Let $G=\left(G_{1} \mid G_{2}\right)$ and $G^{\prime}=\left(G_{1}^{\prime} \mid G_{2}^{\prime}\right)$ be two link redundancies with the same largest horizontal link redundancy. If $G_{1} \subseteq G_{1}^{\prime}$ and $G_{2} \subseteq G_{2}^{\prime}$ and $W_{F}$ and $W_{F}^{\prime}$ are the corresponding widest fault windows, then $\left\|W_{F}\right\| \geqslant\left\|W_{F}^{\prime}\right\|$.

Proof. In Algorithm UCFP, we note that there will be more $X$ 's for $G^{\prime}$ than for $G$ in each floor, so the algorithm terminates sooner. Hence the final value of $f$ for $G^{\prime}$ will be less than or equal to the final value of $f$ for $G$. Note that the width of window of a CFP increases as the final value of $f$ increases. Hence the theorem follows.

We now present some results which give the maximum width of a window of a CFP when there are at most two horizontal and at most two vertical link redundancies and an upper bound for the width of a window of a CFP in the general case. These results follow directly from Conjecture 3.1 and application of Algorithm UCFP.

Result 3.1. Let $G=(1, g \mid 1)$. Then, the maximum width of the window of a CFP with $N_{1} g$ faults is $g\left(N_{1}-1\right)+(g-1)^{2}+1$.

Proof. Let $F$ be the catastrophic fault pattern with respect to link redundancy $G=(1, g \mid 1)$ generated by Algorithm UCFP. Let $F_{i} \subseteq F$ be the set of faulty PEs occur only in the $i$ th row of $A$. Note that all $F_{i}$ 's are identical and of width [10] $(g-1)^{2}+1$. We observe that, if $F_{i}$ begins at PE $(i, j)$ then $F_{i-1}$ will begin at PE $(i-1, j+g)$ for $2 \leqslant i \leqslant N_{1}$ and Algorithm UCFP starts at PE $\left(N_{1}, 1\right)$. Therefore $F$ begins at PE ( $N_{1}, 1$ ) and ends at PE $\left(1, g\left(N_{1}-1\right)+(g-1)^{2}+1\right)$.

Result 3.2. Let $G=(1, g \mid 1, v)$ and $v$ divides ( $N_{1}-1$ ). Then, the maximum width of the window of a CFP with $N_{1} g$ faults is given by $g\left(\left(N_{1}-1\right)\right.$ / $v+v-2)+(g-1)^{2}+1$.

Proof. The proof of the present result is similar to that of previous result. Let $F$ be the catastrophic fault pattern with respect to link redundancy $G=(1, g \mid 1, v)$ generated by Algorithm UCFP. Here we observe that if $F_{r v}, r=1,2, \ldots,\left(N_{1}-1\right) / v$, begins at PE $(r v, j)$ then $F_{i}, r v-v+2 \leqslant i \leqslant r v-1$, will begin at PE $(i, j+g(r v-l))$ and $F_{r v-v+1}$ will begin at PE $(r v-v+1, j)$. Note that Algorithm UCFP starts at PE $\left(N_{1}, 1\right)$. Now it is easy to check that $F$ begins at PE $\left(N_{1}, 1\right)$ and ends at PE $\left(2, g\left(\left(N_{1}-1\right) / v+v-2\right)+\right.$ $\left.(g-1)^{2}+1\right)$.

In view of Theorem 3.3, when $G=\left(1, g_{2}, \ldots, g_{k}\right.$ | $1, v_{2}, \ldots, v_{l}$ ), we get an upper bound for the width of the window of a CFP with $N_{1} g_{k}$ faults by replacing $g$ and $v$ by $g_{k}$ and $v_{l}$, respectively in the expression given in the preceding result. A similar statement holds for Result 3.1.

## 4. Cuboid representation for fault pattern

Suppose we are given a fault pattern $F$ with $N_{1} g_{k}$ faults in a two-dimensional array with link redundancy $G=\left(g_{1}, g_{2}, \ldots, g_{k} \mid v_{1}, v_{2}, \ldots, v_{l}\right)$. Without loss generality we will assume that the first column of A contains a fault. We now consider the cuboid representation of $A$ used in the proof of Theorem 3.1. However, we label the $N_{1}$ rows in any floor of the cuboid
with $0,1, \ldots, N_{1}-1$ instead of $1,2 \ldots, N_{1}$ and $g_{k}$ columns in any floor with $0,1, \ldots, g_{k}-1$ instead of $1,2, \ldots, g_{k}$. The floors are labelled using $0,1,2, \ldots$ as before. With every $\mathrm{PE}(i, j)$ we can uniquely associate the triple $(x, y, z)$ where $x, y$ and $z$ are the row label, column label and floor label of the position $(i, j)$ occupies in the cuboid. (Note that $x=i-1, y$ is the remainder obtained when $j-1$ is divided by $g_{k}$ and $z$ is $\left\lfloor(j-1) / g_{k}\right\rfloor$.) We will write
$W(x, y, z)= \begin{cases}1 & \text { if }(i, j) \in F, \\ 0 & \text { otherwise } .\end{cases}$
We will some time refer to $(x, y, z)$ as the location of the PE ( $i, j$ ).

Suppose now $F$ is a fault pattern such that for any $(x, y)$, there is exactly one $z$ for which $W(x, y, z)=1$ (i.e., there is exactly one faulty PE in each pillar). We then denote this $z$ by $h_{x y}$ and call the matrix
$H=\left(\begin{array}{cccc}h_{00} & h_{01} & \cdots & h_{0, g_{k}-1} \\ h_{10} & h_{11} & \cdots & h_{1, g_{k}-1} \\ \vdots & \vdots & & \vdots \\ h_{N_{1}-1,0} & h_{N_{1}-1,1} & \cdots & h_{N_{1}-1, g_{k}-1}\end{array}\right)$
the height matrix of $F$.
Example 4.1. Consider the CFP $F=\{(1,5),(1,8)$, $(1,11),(1,14),(2,9),(2,12),(2,15),(2,18),(3,5)$, $(3,8),(3,11),(3,14),(4,1),(4,4),(4,7),(4,10)\}$ with 16 faults for a two-dimensional array $A$ with link redundancy $G=(1,4 \mid 1,3)$ which has $\left\|W_{F}\right\|=18$ as shown in Fig. 5.

The height matrix for this CFP is
$H=\left(\begin{array}{llll}1 & 3 & 2 & 1 \\ 2 & 4 & 3 & 2 \\ 1 & 3 & 2 & 1 \\ 0 & 2 & 1 & 0\end{array}\right)$.
Note that every minimal CFP (i.e., CFP with $N_{1} g_{k}$ faulty PEs) satisfies the conditions stated at the beginning of the preceding paragraph. We now define the interior, exterior and border elements in the cuboid representation of a minimal CFP.

Definition 4.1. Let $F$ be a minimal CFP. Then the PE of $A$ corresponding to the location $(x, y, z)$ is said to be interior, border or exterior with respect to $F$ according as $z<h_{x y}, z=h_{x y}$ or $z>h_{x y}$. The interior $I(F)$ of $F$, the border $B(F)$ of $F$ and the exterior
$E(F)$ of $F$ are defined to be the set of all interior elements, the set of all border elements and the set of all exterior elements of $F$, respectively.

Lemma 4.1. A fault pattern $F$ is catastrophic for a two-dimensional network $\mathcal{N}$ with unidirectional link redundancy $G$ iff it is not possible to reach any exterior element from any interior element using the links in $\mathcal{N}$.

Proof. It is easy to see that all interior elements are reachable from ICUL and ICUR is reachable from all exterior elements. The lemma follows from Definition 2.5.

## 5. Necessary and sufficient conditions for catastrophic fault patterns

We give the necessary and sufficient conditions for the existence of minimal CFPs in terms of the height matrix.

Proposition 5.1. An $N_{1} \times g_{k}$ matrix $H=\left(\left(h_{x y}\right)\right)$ with non-negative integer entries is the height matrix of a minimal CFP for $\mathcal{N}$ with unidirectional link redundancy $G=(1, g \mid 1)$ iff the following conditions are satisfied:
(i) $h_{x 0}-h_{x, g-1}=0$ or +1 for all $x$ such that $0 \leqslant$ $x \leqslant N_{1}-1 ; h_{x 0}=h_{x, g-1}=0$ for at least one $x$.
(ii) $h_{x y}-h_{x, y+1} \leqslant 1$ whenever $0 \leqslant x \leqslant N_{1}-1$ and $0 \leqslant y \leqslant g-2$ and
(iii) $h_{x y}-h_{x+1, y} \leqslant 1$ whenever $0 \leqslant x \leqslant N_{1}-2$ and $0 \leqslant y \leqslant g-1$.

Clearly the number of minimal CFPs for $\mathcal{N}$ with unidirectional link redundancy $G=(1, g \mid 1)$ is equal to the number of height matrices $H$ which satisfy the conditions of Proposition 5.1. We shall illustrate Proposition 5.1 by an example.

Example 5.1. Consider the fault pattern $F=\{(1,1)$, $(1,4),(1,7),(1,14),(2,1),(2,7),(2,8),(2,10)$, $(3,5),(3,8),(3,11),(3,14)\}$ in a $3 \times 20$ array $A$ with link redundancy $G=(1,4 \mid 1)$. Note that in the cuboid representation for $F$ there is exactly one faulty PE in each pillar.


Fig. 6. Cuboid representation of $F$.
The height matrix for this fault pattern is
$H=\left(\begin{array}{llll}h_{00} & h_{01} & h_{02} & h_{03} \\ h_{10} & h_{11} & h_{12} & h_{13} \\ h_{20} & h_{21} & h_{22} & h_{23}\end{array}\right)=\left(\begin{array}{llll}0 & 3 & 1 & 0 \\ 0 & 2 & 1 & 1 \\ 1 & 3 & 2 & 1\end{array}\right)$.
Note that, $h_{10}-h_{13}=-1$ which violates condition (i) of Proposition 5.1. We see from Fig. 6, that the exterior processor at location $(1,0,1)$ and the interior processor at location $(1,3,0)$ are connected by a horizontal regular link. Hence $F$ is not a catastrophic fault pattern by Lemma 4.1. Similarly condition (ii) of Proposition 5.1 is violated since $h_{01}-h_{02}=2$. Note that, locations $(0,1,2)$ and $(0,2,2)$ contain an interior processor and an exterior processor, respectively, which are connected by a horizontal regular link. However it can easily verified that $F$ satisfied conditions (iii) of Proposition 5.1 even though $F$ is not catastrophic.

In the general case, we have the following proposition:

Proposition 5.2. An $N_{1} \times g_{k}$ matrix $H=\left(\left(h_{x y}\right)\right)$ with non-negative integer entries is the height matrix of a minimal CFP for $\mathcal{N}$ with unidirectional link redundancy $G=\left(g_{1}, g_{2} \ldots, g_{k} \mid v_{1}, v_{2}, \ldots, v_{l}\right)$ iff the following conditions are satisfied:
(i) $h_{x 0}-h_{x, g_{k}-1}=0$ or +1 for all $x$ such that $0 \leqslant$ $x \leqslant N_{1}-1 ; h_{x 0}=h_{x, g_{k}-1}=0$ for at least one $x$.
(ii) $h_{x y}-h_{x, y+g_{i}} \leqslant 1$ for all $g_{i}, 1 \leqslant i \leqslant k-1$ whenever $0 \leqslant x \leqslant N_{1}-1$ and $0 \leqslant y \leqslant g_{k}-g_{i}-1$ and
(iii) $h_{x y}-h_{x+v_{i}, y} \leqslant 1$ for all $v_{i}, 1 \leqslant i \leqslant l$ whenever $0 \leqslant x \leqslant N_{1}-v_{i}-1$ and $0 \leqslant y \leqslant g_{k}-1$.

As before, the number of minimal CFPs for $\mathcal{N}$ with unidirectional link redundancy $G=\left(g_{1}, g_{2} \ldots, g_{k} \mid\right.$ $v_{1}, v_{2}, \ldots, v_{l}$ ) is equal to the number of height matrices $H$ which satisfy the conditions of Proposition 5.2.

## 6. Conclusions

In this paper, we extended the characterization of CFPs known for linear arrays to two-dimensional VLSI arrays. We determined the minimum number of faults required for a fault pattern to be catastrophic. We gave an algorithm for the construction of minimal CFPs with largest fault window when all the links are unidirectional, and studied the effect of different link configurations on the size of the fault window. We gave the necessary and sufficient conditions for the existence of CFPs. However, the number of catastrophic fault patterns is not known even for the link redundancy $G=(1, g \mid \mathbf{1}, v)$.

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## References

[1] V. Balasubramanian, P. Banerjee, A fault tolerant massively parallel processing architecture, J. Parallel Distrib. Comput. 4 (1987) 363-383.
[2] J. Bruck, R. Cypher, C.T. Ho, Fault-tolerant meshes with minimal number of spares, in: Proc. of 3rd IEEE Symposium on Parallel and Distributed Processing, 1991, pp. 288-295.
[3] R. De Prisco, A. Monti, L. Pagli, Efficient testing and reconfiguration of VLSI linear arrays, Theoret. Comput. Sci. 197 (1998) 105-129.
[4] R. De Prisco, A. De Santis, Catastrophic faults in reconfigurable systolic linear arrays, Discrete Appl. Math. 75 (1997) 105-123.
[5] H.T. Kung, Why systolic architecture?, IEEE Comput. 15 (1982) 37-46.
[6] S. Maity, B. Roy, A. Nayak, On enumeration of catastrophic fault patterns, Inform. Process. Lett. 81 (2002) 209-212.
[7] T. Leighton, C.E. Leiserson, Wafer-scale integration of systolic arrays, IEEE Trans. Comput. C-34 (1985) 448-461.
[8] A. Nayak, L. Pagli, N. Santoro, Efficient construction of catastrophic patterns for VLSI reconfigurable arrays, Integration: The VLSI Journal 15 (1993) 133-150.
[9] A. Nayak, L. Pagli, N. Santoro, On testing of catastrophic faults in reconfigurable arrays with arbitrary link redundancy, Integration: The VLSI Journal 20 (1996) 327-342.
[10] A. Nayak, N. Santoro, R. Tan, Fault-Intolerance of reconfigurable systolic arrays, in: Proc. of 20th Internat. Symp. on Fault-Tolerant Computing, 1990, pp. 202-209.
[11] L. Pagli, G. Pucci, Counting the number of fault patterns in redundant VLSI arrays, Inform. Process. Lett. 50 (1994) 337342.


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